WHAT WE CLAIM ARE:

1. An audio data recording medium reproducer comprising:

a medium reader for reading-out digital audio data at a speed faster than the audio data reproducing rate from a medium into which the data has been recorded;

a first buffer memory for buffering the data read by the medium reader;

a first controller for controlling the medium reader and controlling reading and writing of the first buffer memory;

a second buffer memory for buffering data transferred from the first buffer memory;

a DA converter which receives digital data from the second buffer memory and converts the data into analog audio signals; and

a second controller for controlling reading and writing of the second buffer memory, wherein

the first controller and second controller are connected via an interface.

2. The audio data recording medium reproducer according to Claim 1, wherein the medium reader, first buffer memory, and first controller are constructed as a single unit.

3. The audio data recording medium reproducer according to Claim 1, wherein a third controller for controlling a user interface is provided separately from the second controller.

4. The audio data recording medium reproducer according to Claim 1, wherein the medium into which the digital audio data has been recorded is a compact disk.

5. An audio data recording medium reproducer comprising:

a medium reader for reading-out digital audio data from a medium, into

which the data has been recorded, at a speed faster than the audio data normal reproducing rate;

a first buffer memory for buffering data read by the medium reader;

a first controller for controlling the medium reader and controlling reading and writing of the first buffer memory;

a second buffer memory for buffering data transferred from the first buffer memory;

a DA converter which receives data from the second buffer memory and DA-converts it into analog audio signals for normal speed reproduction; and

a second controller for controlling reading and writing of the second buffer memory, wherein

the first controller and second controller are connected via an interface, and data transfer between the first buffer memory and second buffer memory is intermittently performed.